

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Kian et al.

Application No.: Unassigned

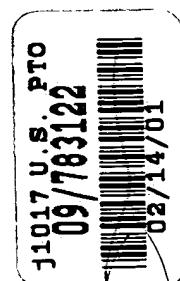
Filed: February 14, 2001

For: Method For Patterning A Multilayered Conductor/Substrate Structure

Group No.: Unassigned

Examiner: Unassigned

Assistant Commissioner for Patents
Washington, D.C. 20231



INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. § 1.56, the references listed on the attached Form PTO-1449 are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited references are enclosed.

This information disclosure statement is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. 37 C.F.R. 1.97(b).

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached Form PTO-1449 with initials or other appropriate marks, and that the references be made of record as cited references in the application.

Date: February 14, 2001

Respectfully submitted,

SIGNATURE OF PRACTITIONER

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